## Module description

| Module title   |                 |   |                                  | Abbreviation  |
|--|-----------------|---|----------------------------------|---------------|
| Principles of Electronics (with Practical Course)  |                 |   |                                  | 11-N2-092-m01 |
| Module coordinator   |                 |   | Module offered by                |               |
| Managing Director of the Institute of Applied Physics  |                 |   | Faculty of Physics and Astronomy |               |
| ECTS Method of grading   |                 | Only after succ. compl. of module(s)  |                                  |               |
| 6 nume   | numerical grade |   |                                  |               |
| Duration Module level  |                 | Other prerequisites   |                                  |               |
| 1 semester undergraduate   |                 | Certain prerequisites must be met to qualify for admission to as-<br>sessment. The lecturer will inform students about the respective details<br>at the beginning of the course. Registration for the course will be con-<br>sidered a declaration of will to seek admission to assessment. If stu-<br>dents have obtained the qualification for admission to assessment over<br>the course of the semester, the lecturer will put their registration for as-<br>sessment into effect. Students who meet all prerequisites will be admit-<br>ted to assessment in the current or in the subsequent semester. For as-<br>sessment at a later date, students will have to obtain the qualification for<br>admission to assessment anew. |                                  |               |
| Contents   |                 |   |                                  |               |
| Principles of electronic components and circuits. Analogous circuit technology: Passive (resistors, capacitors, coils and diodes) and active components (bipolar and field-effect transistors as well as operational amplifiers). Digital circuits: different types of gates and CMOS circuits. Microcontroller Intended learning outcomes The students have knowledge of the practical setup of electronic circuits from the field of analogous and digital circuit technology. |                 |   |                                  |               |
| Courses (type, number of weekly contact hours, language — if other than German)  |                 |   |                                  |               |
| V + P (no information on SWS (weekly contact hours) and course language available)   |                 |   |                                  |               |
| <b>Method of assessment</b> (type, scope, language — if other than German, examination offered — if not every semester, information on whether module is creditable for bonus)   |                 |   |                                  |               |
| written examination (approx. 90 minutes)<br>Assessment offered: When and how often assessment will be offered depends on the method of assessment<br>and will be announced in due form under observance of Section 32 Subsection 3 ASPO (general academic and<br>examination regulations) 2009.  |                 |   |                                  |               |
| Allocation of places   |                 |   |                                  |               |
| Only as part of pool of general key skills (ASQ): 15 places. Places will be allocated by lot.  |                 |   |                                  |               |
| Additional information   |                 |   |                                  |               |
|  |                 |   |                                  |               |
| Workload   |                 |   |                                  |               |
|  |                 |   |                                  |               |
| Teaching cycle   |                 |   |                                  |               |
|  |                 |   |                                  |               |
| Referred to in LPO I (examination regulations for teaching-degree programmes)  |                 |   |                                  |               |
|  |                 |   |                                  |               |
| Module appears in<br>Bachelor' degree (1 major) Nanostructure Technology (2010)  |                 |   |                                  |               |
| Bachelor's degree (1 major, 1 minor) Physics (Minor, 2010)   |                 |   |                                  |               |
|  |                 | ·ysics (millor, 2010)   |                                  |               |

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