

<b>Module title</b>		<b>Abbreviation</b>
Selected Topics Prototype Design and Implementation		10-I=STPDI-182-m01
<b>Module coordinator</b>		<b>Module offered by</b>
holder of the Chair of Computer Science VII		Institute of Computer Science
<b>ECTS</b>	<b>Method of grading</b>	<b>Only after succ. compl. of module(s)</b>
5	numerical grade	--
<b>Duration</b>	<b>Module level</b>	<b>Other prerequisites</b>
1 semester	graduate	--
<b>Contents</b>		
Selected topics in prototype design und implementation.		
<b>Intended learning outcomes</b>		
The students possess an advanced knowledge in the area of prototype design and implementation. They are able to understand solutions to complex problems in this area and to transfer them to related questions.		
<b>Courses</b> (type, number of weekly contact hours, language — if other than German)		
V (2) + Ü (2) Module taught in: English		
<b>Method of assessment</b> (type, scope, language — if other than German, examination offered — if not every semester, information on whether module is creditable for bonus)		
a) written examination (approx. 90 to 120 minutes) orb) project (project documentation approx. 20 pages with presentation 30 to 45 minutes and subsequent discussion on the topic) or cb) oral examination of one candidate each (approx. 20 minutes) or d) oral examination in groups (groups of up to 2 candidates, approx. 15 minutes per candidate) Language of assessment: English creditable for bonus		
<b>Allocation of places</b>		
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<b>Additional information</b>		
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<b>Workload</b>		
150 h		
<b>Teaching cycle</b>		
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<b>Referred to in LPO I</b> (examination regulations for teaching-degree programmes)		
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<b>Module appears in</b>		
Master's degree (1 major) Satellite Technology (2018)		