

<b>Module title</b>		<b>Abbreviation</b>
Embedded Systems		10-I=ES-152-m01
<b>Module coordinator</b>		<b>Module offered by</b>
holder of the Chair of Computer Science V		Institute of Computer Science
<b>ECTS</b>	<b>Method of grading</b>	<b>Only after succ. compl. of module(s)</b>
8	numerical grade	--
<b>Duration</b>	<b>Module level</b>	<b>Other prerequisites</b>
1 semester	graduate	--
<b>Contents</b>		
Models of embedded systems, implementation methods (ASIC, AISIP, micro controller), verification of embedded systems, implementation planning static, periodic and dynamic, binding problems, hardware synthesis, software synthesis.		
<b>Intended learning outcomes</b>		
The students are familiar with the technical possibilities for the design of embedded systems and master the most important techniques for the modelling, verification and optimisation of such systems in hardware and software.		
<b>Courses</b> (type, number of weekly contact hours, language – if other than German)		
V (4) + Ü (2)		
<b>Method of assessment</b> (type, scope, language – if other than German, examination offered – if not every semester, information on whether module is creditable for bonus)		
written examination (approx. 60 to 120 minutes). If announced by the lecturer at the beginning of the course, the written examination may be replaced by an oral examination of one candidate each (approx. 20 minutes) or an oral examination in groups of 2 candidates (approx. 15 minutes per candidate). Language of assessment: German and/or English creditable for bonus		
<b>Allocation of places</b>		
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<b>Additional information</b>		
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<b>Workload</b>		
240 h		
<b>Referred to in LPO I</b> (examination regulations for teaching-degree programmes)		
§ 22 II Nr. 3b		
<b>Module appears in</b>		
First state examination for the teaching degree Gymnasium Computer Science (2015)		