

Module title		Abbreviation
Embedded Systems		10-I=ES-141-m01
Module coordinator		Module offered by
holder of the Chair of Computer Science V		Institute of Computer Science
ECTS	Method of grading	Only after succ. compl. of module(s)
8	numerical grade	--
Duration	Module level	Other prerequisites
1 semester	graduate	--
Contents		
Models of embedded systems, implementation methods (ASIC, AISIP, micro controller), verification of embedded systems, implementation planning static, periodic and dynamic, binding problems, hardware synthesis, software synthesis.		
Intended learning outcomes		
The students are familiar with the technical possibilities for the design of embedded systems and master the most important techniques for the modelling, verification and optimisation of such systems in hardware and software.		
Courses (type, number of weekly contact hours, language – if other than German)		
V + Ü (no information on SWS (weekly contact hours) and course language available)		
Method of assessment (type, scope, language – if other than German, examination offered – if not every semester, information on whether module is creditable for bonus)		
written examination (60 to 120 minutes); if announced by the lecturer at the beginning of the course, the written examination can be replaced by an oral examination of one candidate each (approx. 20 minutes) or an oral examination in groups (groups of 2, approx. 30 minutes) Language of assessment: German, English		
Allocation of places		
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Additional information		
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Workload		
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Teaching cycle		
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Referred to in LPO I (examination regulations for teaching-degree programmes)		
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Module appears in		
Master's degree (1 major) Computer Science (2014)		