<table>
<thead>
<tr>
<th>Module title</th>
<th>Abbreviation</th>
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<tr>
<td><strong>Embedded Systems</strong></td>
<td><strong>10-I=ES-141-m01</strong></td>
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**Module coordinator**  
holder of the Chair of Computer Science V

**Module offered by**  
Institute of Computer Science

**ECTS**  
8

**Method of grading**  
numerical grade

**Only after succ. compl. of module(s)**  
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**Duration**  
1 semester

**Module level**  
graduate

**Other prerequisites**  
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**Contents**
Models of embedded systems, implementation methods (ASIC, AISIP, micro controller), verification of embedded systems, implementation planning static, periodic and dynamic, binding problems, hardware synthesis, software synthesis.

**Intended learning outcomes**
The students are familiar with the technical possibilities for the design of embedded systems and master the most important techniques for the modelling, verification and optimisation of such systems in hardware and software.

**Courses**
V + Ü (no information on SWS (weekly contact hours) and course language available)

**Method of assessment**
written examination (60 to 120 minutes); if announced by the lecturer at the beginning of the course, the written examination can be replaced by an oral examination of one candidate each (approx. 20 minutes) or an oral examination in groups (groups of 2, approx. 30 minutes)

Language of assessment: German, English

**Allocation of places**
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**Additional information**
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**Referred to in LPO I**
(examination regulations for teaching-degree programmes)

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**Module appears in**
Master's degree (1 major) Computer Science (2014)