

Module title		Abbreviation
Digital computer systems		10-I-RAL-072-m01
Module coordinator		Module offered by
holder of the Chair of Computer Science V		Institute of Computer Science
ECTS	Method of grading	Only after succ. compl. of module(s)
8	numerical grade	--
Duration	Module level	Other prerequisites
1 semester	undergraduate	--
Contents		
Introduction to digital technologies, Boolean algebras, combinatory circuits, synchronous and asynchronous circuits, hardware description languages, structure of a simple processor, machine programming, memory hierarchy.		
Intended learning outcomes		
The students possess a knowledge of the fundamentals of digital technologies up to the design and programming of easy microprocessors as well as knowledge for the application of hardware description languages for the design of digital systems.		
Courses (type, number of weekly contact hours, language – if other than German)		
V + Ü (no information on SWS (weekly contact hours) and course language available)		
Method of assessment (type, scope, language – if other than German, examination offered – if not every semester, information on whether module is creditable for bonus)		
written examination (80 minutes) or oral examination (one candidate each: 20 minutes, groups of 2: 30 minutes, groups of 3: 40 minutes)		
Allocation of places		
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Additional information		
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Workload		
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Teaching cycle		
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Referred to in LPO I (examination regulations for teaching-degree programmes)		
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Module appears in		
Bachelor' degree (1 major) Computer Science (2007) Bachelor' degree (1 major) Mathematics (2008) Bachelor' degree (1 major) Mathematics (2007) Bachelor' degree (1 major) Computational Mathematics (2009)		