

Module title		Abbreviation
Introduction to Core Avionics Hardware		10-I-MEC-141-m01
Module coordinator		Module offered by
holder of the Chair of Computer Science VIII		Institute of Computer Science
ECTS	Method of grading	Only after succ. compl. of module(s)
10	numerical grade	--
Duration	Module level	Other prerequisites
1 semester	undergraduate	--
Contents		
Fundamental principles of data processing, especially for aerospace applications. What is information? Guidance for reliable systems, analogue, digital, FPGAs, radiation effects, micro programming, CPUs, DMAs, memory, memory organisation, system architecture, input and output, sensors and actuators, energy systems, reliability, fault tolerance. Programming of embedded systems in C++.		
Intended learning outcomes		
Understanding of analogue and digital data processing in embedded systems. Structure of hardware and programming. Embedded programming in C++, knowledge about common sensors and actuators as well as input and output systems.		
Courses (type, number of weekly contact hours, language – if other than German)		
V + Ü + Ü (no information on SWS (weekly contact hours) and course language available)		
Method of assessment (type, scope, language – if other than German, examination offered – if not every semester, information on whether module is creditable for bonus)		
written examination (approx. 120 minutes) and approx. 6 practical exercises (approx. 6 exercises, approx. 4 hours each), weighted 1:1		
Allocation of places		
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Additional information		
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Referred to in LPO I (examination regulations for teaching-degree programmes)		
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Module appears in		
Bachelor' degree (1 major) Aerospace Computer Science (2014)		